# Responsivity–Resistance Relationship in MIIM Diodes

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Abstract—The metal-insulator-insulator-metal (MIIM) systems of  $Co/Co_3O_4/TiO_2/Ti$  and Ni/NiO/TiO\_2/Cr exhibit among the best reported combinations of the desirable quantities of high responsivity and low resistance. The relationship between responsivity and resistance in MIIM diodes is developed by fabricating and measuring hundreds of diodes of these compositions. For both diodes, higher responsivities are associated with higher resistances, which confirms a theoretical prediction for MIIM diode characteristics. Simulation of diode behavior using a transfer-matrix method diode simulator confirms the results and is consistent with nonresonant tunneling behavior.

*Index Terms*—Energy harvesting, metal–insulator–insulator– metal (MIIM) diode, rectennas, responsivity.

### I. INTRODUCTION

**T** E REPORT on the relationship between responsivity and resistance in metal-insulator-insulator-metal (MIIM) diodes. These diodes are suitable for ultra-high frequency operation in optical rectennas to convert infrared radiation into electrical power [1]. Optical rectennas combine submicron antennas with ultra-high speed diodes to rectify incident radiation. The optical frequency signal from the antenna is rectified by the diode and flows through a low bandpass dc voltage filter to the load. This circuit acts as a diode clamp, which raises the output dc voltage to as high as the peak input ac voltage. Both metal/insulator/metal (MIM) and MIIM diodes have been investigated for this application. It is desirable to have low resistance (R) and capacitance (C) in these diodes to minimize the RC time constant of the device and to match the diode impedance to that of the antenna for efficient power transfer [2]. It is also desirable to have a large responsivity [(dc) out per unit ac power in].

However, a tradeoff between low resistance and high responsivity has been shown in MIM diodes, as large barrier heights result in high responsivity but at the expense of high resistance

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Fig. 1. Cross sectional schematic of the Co/Co<sub>3</sub>O<sub>4</sub>/TiO<sub>2</sub>/Ti diode.

[3], [4]. There has been a theoretical prediction that MIIM diodes could provide higher responsivities with lower resistances but it has not been verified experimentally in a systematic way [5]. We systematically fabricated and tested  $Co/Co_3O_4/TiO_2/Ti$  and Ni/NiO/TiO<sub>2</sub>/Cr diodes, as these material combinations have the potential to give the low resistance required to match the antenna while providing good responsivities. In order to measure hundreds of these devices, we compare their characteristics, show the effects of annealing, establish a relationship between responsivity and resistance of MIIM diodes formed from these two materials systems, and use simulations to infer the transport mechanism.

## II. FABRICATION AND CHARACTERIZATION

## A. Fabrication

Diodes formed from Co/Co<sub>3</sub>O<sub>4</sub>/TiO<sub>2</sub>/Ti show particularly promising current-voltage characteristics. We have previously described the fabrication and initial properties of these diodes, [6] and briefly summarize the fabrication sequence here. A schematic of the device is shown in Fig. 1. Devices were fabricated on 100-mm-diameter silicon wafers coated with a SiO<sub>2</sub> film. Evaporated cobalt films were patterned by a lift-off process to form the lower electrode. Cobalt oxide films were grown by plasma oxidation of the metal at a power of 30 W in an O<sub>2</sub> environment at a pressure of 50 mtorr. The Co<sub>3</sub>O<sub>4</sub> thickness was measured by variable angle spectroscopic ellipsometry and the phase was previously confirmed by X-ray photoelectron spectroscopy [6]. The TiO<sub>2</sub> films were deposited on the Co<sub>3</sub>O<sub>4</sub> films by reactive sputtering at a pressure of 3 mtorr in an environment of 60% O<sub>2</sub> and 40% Ar, and a power of 60 W, for various

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times. The TiO<sub>2</sub> film thickness was estimated by depositing a much thicker film, measuring the thickness, and extracting the deposition rate. A 5-nm-thick titanium film was evaporated onto the TiO<sub>2</sub>, which was followed by a 200-nm-thick sputtered niobium film. The Co<sub>3</sub>O<sub>4</sub>/TiO<sub>2</sub>/Ti /Nb stack was then patterned and etched into a circular pillar on top of the Co electrode.

A passivating layer of SiO2 was deposited by plasmaenhanced chemical vapor deposition at 190 °C and then partially removed by chemo-mechanical polishing, which exposes the top of the Nb pillar. Contacts to the bottom Co and upper Nb were made with Ti/A1. The resulting diode area was 0.071  $\mu$ m<sup>2</sup> for the Co/Co<sub>3</sub>O<sub>4</sub>/TiO<sub>2</sub>/Ti diodes. For the Ni/NiO/TiO<sub>2</sub>/Cr diodes, the fabrication was the same with the following exceptions: A nickel film replaced the cobalt film for the lower electrode, a NiO film was also grown in an O<sub>2</sub> ambient, evaporated chromium was the contact to the TiO<sub>2</sub> film, and the passivating SiO<sub>2</sub> was deposited by sputtering at room temperature. Due to the use of sputtered SiO<sub>2</sub> for passivation, the Ni-based diodes had a lower thermal exposure as-fabricated compared with the Co-based diodes. The maximum temperature during Ni diode fabrication was 135 °C. However, the as-fabricated Ni/NiO/TiO2/Cr diodes only became functional after they were annealed to temperatures of 200 °C or greater. All other films and fabrications were the same. Due to difficulties in etch, only the larger 1  $\mu$ m<sup>2</sup> Ni/NiO/TiO<sub>2</sub>/Cr diodes yielded working devices.

## B. Characterization

Current–voltage measurement sweeps were done from -0.3 to +0.3 V, as the devices operate near zero bias in energy-harvesting applications. The data were fitted by a seventh-order polynomial to reduce the effects of measurement noise, which would distort the second derivate of the curve. Figures of merit for these diodes are zero-bias responsivity (ZBR), which is defined as

$$\beta_0 = \left[\frac{d^2 I}{2d^2 V}\right]_{V=0}$$

and zero bias resistance, which is defined as

$$R_D = \left[\frac{dV}{dI}\right]_{V=0}$$

## C. Results

Six wafers with different  $Co_3O_4/TiO_2$  thicknesses were fabricated and measured. Fifteen to twenty-two devices were measured on each wafer with resultant median and 1 sigma distributions of responsivity and resistance calculated. Table I summarizes the values of responsivity and resistance for each wafer immediately after fabrication:

There was variation among the diodes measured, as shown in Fig. 2 by the distributions for wafer 3. These variations can arise from a variation in the diode oxide thickness.

It has been shown previously that annealing these diodes can dramatically reduce resistance along with a much smaller change in the responsivity [6]. The comparison of the as-fabricated diodes that saw a maximum temperature of 190  $^{\circ}$ C with those annealed at a temperature of 255  $^{\circ}$ C reveals that the

TABLE I COMPARISON OF CO/CO<sub>3</sub>O<sub>4</sub>/TiO<sub>2</sub>/Ti Diode Characteristics With Various Oxide Thickness

#	Co <sub>3</sub> O <sub>4</sub> (nm)	TiO <sub>2</sub> (nm)	Median $R_{\rm D} \pm 1$ sigma @ 0 V ( $\Omega$ )	Median $\beta_0 \pm 1$ sigma @ 0 V (A/W)
1 2 3 4 5	1.2 1.4 1.4 1.4 1.5	2.0 1.6 2.0 2.4 1.2	$\begin{array}{c} 1600 \pm 400 \\ 300 \pm 80 \\ 2400 \pm 1650 \\ 15\ 000 \pm 13\ 400 \\ 200 \pm 80 \end{array}$	$\begin{array}{c} 0.1 \pm 0.16 \\ 0.8 \pm 0.09 \\ 1.2 \pm 0.18 \\ 0.8 \pm 0.49 \\ 0.5 \pm 0.09 \end{array}$
6	1.5	1.6	$1700 \pm 400$	$1.0\pm0.16$



Fig. 2. Distributions of resistance (left) and responsivity (right) in actual  $Co/Co_3O_4/TiO_2/Ti$  diodes from wafer 2.

responsivity decreases ~0.5 times, whereas the resistance decreases ~0.1 times, as shown in the example in Fig. 3. The origin of the resistance that decreases with annealing is unknown. Both  $Co_3O_4$  and  $TiO_2$  films have increased crystallinity and grain size when annealed between 175 and 300 °C [7]–[10]. Both films are wide bandgap semiconductors [11], [12]. Increased grain size in polycrystalline semiconductors has been associated with decreased trap density and higher conductivity [13]. It is possible that annealing these devices has had the same effect in these films.

A scatterplot of the resistance and responsivity for all the Co-based devices measured is shown in Fig. 4. Diodes from wafers 2, 4, 5, and 6 were annealed for 10 min at 222 °C in air to decrease resistance. From the scatterplot, it is apparent that the higher responsivity diodes have higher resistances. Diodes with zero bias responsivities (ZBR) greater than 1.5 A/W all had resistances greater than 1000  $\Omega$ .

Even with the variation among the devices, the dotted line shows that diodes with highest responsivity necessarily come with high resistance. As compared with the results by Bean *et al.* [4] of a resistance–responsivity relationship for MIM diodes, the MIIM diode achieves higher responsivity with either a similar or lower resistance, as shown here. With resonant tunneling MIIM diodes, the responsivity increases and the resistance decreases in the low resistance range of interest, a trend that cannot be achieved with MIM diodes [5].

A second type of diode, Ni/NiO/TiO<sub>2</sub>/Cr, was also studied. Single insulator diodes that are formed with Ni/NiO are desirable because the low Ni/NiO barrier results in low resistance,



Fig. 3. Median responsivities (upper) and resistances (lower) at 0 V, with 1 sigma error bars, of actual  $Co/Co_3O_4/TiO_2/Ti$  diodes from wafer 2 after 10 min anneal in air at the temperature indicated.



Fig. 4. Plot of resistance and responsivity at 0 V of actual 0.071  $\mu m^2$  Co/Co<sub>3</sub>O<sub>4</sub>/TiO<sub>2</sub>/Ti diodes.

which provides a good impedance match with the antenna and a tolerable *RC* time constant [14]. The addition of the second insulator increases the diode responsivity.

The Ni-based diodes showed different behavior after annealing, with responsivity peaking after a 243 °C anneal, and then declining, and the resistance initially increasing up to 234 °C



Fig. 5. Median responsivities (upper) and resistances (lower) at 0 V, with 1 sigma error bars, of actual Ni/NiO/TiO<sub>2</sub>/Cr diodes after 10 min anneals in air at the temperature indicated.



Fig. 6. Plot of resistance and responsivity at 0 V of actual 1  $\mu m^2$  Ni/NiO/ TiO\_/Cr diodes.

and then leveling off with the increase of the anneal temperature, as shown in Fig. 5. The behavior with annealing is different than for the  $Co/Co_3O_4/TiO_2/Ti$  diodes. A scatterplot of the resistance and responsivity for the Ni/NiO/TiO\_2/Cr devices is shown in Fig. 6, with all the diodes in the plot having been annealed. The Ni diodes have a similar relationship between responsivity and resistance as the Co diodes. The relationship of higher responsivity coincident with higher resistance is not unique to either diode.

TABLE II MATERIAL PARAMETERS USED IN SIMULATION

Metal	Φ- fitted (eV)	Φ - [16] (eV)	Insulator	$\chi$ - fitted (eV)	χ (eV)
Co	4.8	5.0	Co <sub>3</sub> O <sub>4</sub>	4.6	N/A
Ti	4.5	4.33	TiO <sub>2</sub>	4.2	4.1 [17]
Ni	5.0	5.04-5.35	NiO	4.9	4.9 [18]
Cr	4.4	4.5			

As the Ni-based diodes have 14 times larger cross section than that of the Co-based diodes, a fair comparison would be to normalize the resistance by increasing the Ni-based diodes' resistance by a similar amount. With normalized resistance to a cross section of 0.071  $\mu$ m<sup>2</sup>, the highest responsivity Ni-based diode has a responsivity of ~1 A/W and a resistance of 56 kΩ, whereas the highest responsivity Co-based diode has a responsivity of 2.2 A/W and a resistance of 18 kΩ. When adjusted for area, the Co-based diodes have a higher responsivity for a given resistance compared with the Ni-based diodes.

The results from both the Co- and Ni-based diodes confirm the earlier theoretical prediction that MIIM diodes can achieve higher responsivities than MIM diodes [5]. A previous publication has compared responsivities and resistances of various MIM and MIIM diodes, normalized to the cross sectional area of the diodes [6].

## D. Simulation

Using a quantum mechanical MIM diode simulator [15], the thickness of each oxide was varied to observe the responsivity-resistance relationship for both  $Co/Co_3O_4/TiO_2/Ti$  and Ni/NiO/TiO<sub>2</sub>/Cr MIIM diodes. The parameters for the materials used in the simulation and reported literature values of same are shown in Table II. The fitted parameters are extracted from the best fit to the fabricated diodes. As the work function is a property of the surface of the metal, it is expected to vary based on crystalline face or faces as well as contamination of the surface. Differences between the fitted work functions and literature values are, therefore, expected and are within normal variation.

For the Co/Co<sub>3</sub>O<sub>4</sub>/TiO<sub>2</sub>/Ti diode, the thicknesses of Co<sub>3</sub>O<sub>4</sub> and TiO<sub>2</sub> were varied from 0.7 to 1.2 nm and 0.4 to 1.1 nm, respectively. As can be seen in Fig. 7, the simulated responsivity-resistance relationship matches the observed trend from fabricated diodes shown in Fig. 4. The dashed line is in the same location in both Figs. 4 and 7. For these Co-based MIIM diodes, step tunneling, as opposed to resonant tunneling [15], is dominant, as concluded from the simulation polarity and the responsivity-resistance trend. To achieve a reduction in resistance, the diode must switch to resonant tunneling behavior. Resonant tunneling can be attained by increasing the thickness of  $Co_3O_4$ , but due to the high barriers (0.25 eV on  $Co_2O_3$  side and 0.33 eV on TiO<sub>2</sub>), this requires a much thicker Co<sub>3</sub>O<sub>4</sub> and high bias voltages. A similar analysis follows for the Ni-based diode. For the NiO/TiO<sub>2</sub> diode, the NiO and TiO<sub>2</sub> thicknesses were varied from 0.6 to 1.2 nm and from 1.3 to



Fig. 7. Plot of resistance and responsivity at 0 V of simulated 0.071  $\mu m^2$  Co/Co<sub>3</sub>O<sub>4</sub>/TiO<sub>2</sub>/Ti diodes.



Fig. 8. Plot of resistance and responsivity at 0 V of simulated 1  $\mu m^2$  Ni/NiO/TiO<sub>2</sub>/Cr diodes.

1.7 nm, respectively. Even though the Ni/NiO barrier is lower than the Co/Co<sub>3</sub>O<sub>4</sub> barrier, the thickness ratio of NiO to TiO<sub>2</sub> renders this as a step tunneling diode that follows a similar responsivity–resistance relationship, as shown in Fig. 8. The measured results indicate a wider variation in thickness across the wafer, which pushes the responsivity and resistance values to higher than the simulated maximum of 0.8 A/W and 7.5 k $\Omega$  and lower than the simulated minimum of 0.2 A/W and 1.7 k $\Omega$ .

There remains a desire to achieve higher responsivities with lower resistances for diodes used to rectify THz radiation. The fabrication methods and simulations of the diodes in this study show a fundamental difficulty to achieve higher responsivity without a higher resistance. Different MIIM diode materials are necessary to achieve a higher responsivity with either similar or lower resistance.

## **III.** CONCLUSION

We have determined in two different MIIM diodes that higher ZBR are coincident with higher resistances, which confirms earlier theoretical predictions.

Diodes made from Co/Co<sub>3</sub>O<sub>4</sub>/TiO<sub>2</sub>/Ti provide higher responsivities than diodes made from Ni/NiO/TiO<sub>2</sub>/Cr for a given resistance. Simulation of diode behavior using a transfer-matrix method diode simulator confirms the results, and is consistent with nonresonant step tunneling behavior. The substantially improved characteristics in this report compared with Bean *et al.*'s report [4] are consistent with the improvements that were simulated for double versus single insulator devices [5]. The results and simulations indicate that the MIIM diodes reported here are unlikely to produce significantly higher responsivity while maintaining a low resistance.

Another type of rectifying element, the geometric diode, may be needed to achieve higher responsivity with low resistance [19]. A geometric diode achieves IV asymmetry from the asymmetry of the physical shape of the device. The *RC* time constant can be significantly lower than that of either MIM or MIIM diodes. However, to achieve asymmetry in these diodes has been challenging.

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